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| <b>INFORMATION DISCLOSURE STATEMENT</b><br><b>PTO-1449 (PAGE 1 OF 1)</b> |  |  |  | SERIAL NUMBER 10/698,425     |  | DOCKET NO. P56982 |  |
|  |  |  |  | APPLICANT JUN-KYU CHA et al. |  |                   |  |
|  |  |  |  | FILING DATE 3 November 2003  |  | GROUP 1775        |  |

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 JUN 02 2004  
 PATENTS TRADEMARK OFFICE

| U.S. PATENT DOCUMENTS |                 |       |                  |       |          |             |
|-----------------------|-----------------|-------|------------------|-------|----------|-------------|
| EXAMINER              | DOCUMENT NUMBER | DATE  | NAME             | CLASS | SUBCLASS | FILING DATE |
| Amitt                 | RE37,444        | 11/01 | Kanazawa         |       |          |             |
| Amitt                 | 5,541,618       | 07/96 | Shinoda          |       |          |             |
| Amitt                 | 5,661,500       | 08/97 | Shinoda et al.   |       |          |             |
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| EXAMINER                 | DOCUMENT NUMBER | DATE  | COUNTRY | CLAS | SUBCLASS | YES         | NO |
| Amitt                    | JP 2001-325888  | 11/01 | JAPAN   |      |          | Abstract    |    |
| Amitt                    | JP 2001-043804  | 02/01 | JAPAN   |      |          | Abstract    |    |
| Amitt                    | JP 2917279      | 04/99 | JAPAN   |      |          | Abstract    |    |
|                          | JP 02-148645    | 06/90 |         |      |          |             |    |
|                          | JP 2845183      | 10/98 |         |      |          |             |    |

| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) |  |
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| Amitt  | "Final Draft International Standard", Project No. 47C/61988-1/Ed.1; Plasma Display Panels - Part 1: Terminology and letter symbols, published by International Electrotechnical Commission, IEC. in 2003, and Appendix A - Description of Technology, Annex B - Relationship Between Voltage Terms And Discharge Characteristics; Annex C - Gaps and Annex D - Manufacturing |
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|------------------------|---------------------------|
| EXAMINER: <i>Amitt</i> | DATE CONSIDERED: 10/19/05 |
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